Electronic Ack	knowledgement Receipt	
EFS ID:	4222968	
Application Number:	10072145	
International Application Number:		
Confirmation Number:	1448	
Title of Invention:	Semiconductor device and method of providing regions of low substrate capacitance	
First Named Inventor/Applicant Name:	Guy E. Averett	
Correspondence Address:	ON Semiconductor Patent Administration Dept - MD A700 P.O. Box 62890 - Phoenix AZ 85082-2890 US 6022445603 -	
Filer:	Kevin Brian Jackson/Becky Newnam	
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Attorney Docket Number:	ONS00317	
Receipt Date:	03-NOV-2008	
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Time Stamp:	16:21:24	
Application Type:	Utility under 35 USC 111(a)	
Payment information:		

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Deposit Account

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination	ONS00317RCE_110308.pdf	595249	no	, n
· '	(RCE)		d32314ff31211b6365b33734483a398d4a5 bc707		

Warnings:

Information:

 For Workshood (DTO oct)	6	29912		
 Fee Worksheet (PTO-06)	fee-info.pdf	dd7b4a8c6708e4bb679b29f1c18504d4bes 09c39	no	2

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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